



Docket No.: R2180.0189/P189  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Takaaki Negoro, et al.

Group Art Unit: 2822

Application No.: 10/769,817

Examiner: Ida M. Soward

Filed: February 3, 2004

Allowed: March 7, 2007

For: SEMICONDUCTOR DEVICE HAVING DMOS  
AND CMOS ON SINGLE SUBSTRATE

**COMMENTS ON EXAMINER'S  
STATEMENT OF REASONS FOR ALLOWANCE**

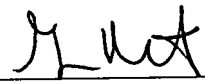
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicants agree in part with the Examiner's statement of reasons for allowance, as provided in the Office Actions mailed December 29, 2005 and June 29, 2006, in that the prior art of record fails to disclose, teach, or suggest the claimed invention. However, Applicants note that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's Reasons for Allowance, including those in the dependent claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.

Dated: June 6, 2007

Respectfully submitted,

By  #41,198  
Thomas J. D'Amico  
Registration No. 28,371  
Steven T. Dickey  
Registration No.: 54,066  
DICKSTEIN SHAPIRO LLP  
1825 Eye Street, NW  
Washington, DC 20006  
(202) 420-2232  
Attorneys for Applicants